Ahmed Fathy Elnokrashy 3 Elsadat st, Taksem Amr Ebn Elaas, Faisal, Giza, Egypt Phone 01111969545 Email: a nokrashy@link.net CAREER OBJECTIVE An active & ambitious career relevant to R&D of medical devices, Biomedical Engineering, Electrical Engineering, Software Engineering and computer science. C++ programming Language MASTER SKILLS C programming Language C# Assembly programming Language OpenGL OpenCL Cq Parallel programming using CUDA Parallel Programming using OpenMP Device driver(WDM) MFC visual studio Multi-Threaded Programming Socket Programming Open CV Real Time Operating system(RTX) DSP PLD design (ALTERA and xilinx tools) VHDL PCB design using Orcad, P-Cad and Spectra Analog and digital circuit design Ultrasound machine design Project planning using Microsoft project Image Processing (Specially Ultrasound images) DICOM linux UNIVERSITY 1993 – 1998 Faculty of Engineering - Cairo University **EDUCATION B.Sc. Systems and Biomedical Engineering.** Completed the college with Very Good degree. Achieved an Excellent evaluation in graduation project. 2007 Faculty of Engineering - Cairo University M.Sc. Systems and Biomedical Engineering. Master in Ultrasound Doppler 3 Elsadat St. – Taksem Amr Ebn Elaas Elawal - Faisal ,Giza -Egypt

2014 Faculty of Engineering - Cairo University				
PHD. Systems and Biomedical Engineering.				
4D Ultrasound Visualization and processing				

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Mastered miscellaneous programming tools pioneerir

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Courses	С		\rightarrow (<i>Excellent</i>)			
	C++		\rightarrow (Excellent)			
	C#		\rightarrow (<i>Excellent</i>)			
	Delphi \rightarrow (<i>Excellent</i>)					
	Multi-Threaded Programming		\rightarrow (<i>Excellent</i>)			
	OpenGL		\rightarrow (<i>Excellent</i>)			
	OpenCL	\rightarrow (very good)				
	CUDA		\rightarrow (<i>Excellent</i>)			
	OpenMP		\rightarrow (<i>Excellent</i>)			
	RTX	RTX				
	VC++	\rightarrow (<i>Excellent</i>)	\rightarrow (Excellent)			
	Socket Programming	\rightarrow (<i>Excellent</i>)				
	Open CV	\rightarrow (very good)				
	Visual Basic		\rightarrow (very good)			
	SQL	\rightarrow (good)				
	Keil compiler (8051, ARM7)		\rightarrow (<i>Excellent</i>)			
	Assembly language		\rightarrow (<i>Excellent</i>)			
	VHDL		\rightarrow (<i>Excellent</i>)			
	Operating system device driver(WD	\rightarrow (Excellent)				
	Operating system device driver(linu	\rightarrow (good)	\rightarrow (good)			
	Linux operating system \rightarrow (god		\rightarrow (good)			
	Related miscellaneous tools					
	IDL	\rightarrow (very good)				
	Matlap	\rightarrow (Very <i>good</i>)				
	Lapview	\rightarrow (very <i>good</i>)				
	The Point Cloud Library (PCL)		\rightarrow (good)			
	Engineering					
	PCB design using Orcad, P-Cad and Spectra					
	PLD programming using ALTERA and xilinx development tools					
Course in the TV & Radio circuitry design and maintenance.						
	Course in Ultrasound system design	and production	(Universal Sonic	s USA) 2000		
Course High-end Ultrasound system design and implementation (TFTRAD Co			TRAD Corp.)2006			
	Course in Data Analysis (<i>statistical science, duke university</i>) with distinction,2014					
PROJECTS	- Under Graduate Projects					
	- Graphics project "Painting tool imp	plementing vecto	r images".	Using VC++.		
	 AI(Artificial Intelligence) project "puzzle" 			Using VC++.		
	- Library Information System project			Using C.		
	- ICU measuring and control system:					
	Implementing a system to measure and control all the environmental					
	parameters of the intensiv	/e care unit (ICU) room (Tempera	ature, light, fire		
	protection, door control, r	ioise detection) t	nis system interf	raced to the PC using		
		e Elowal Fairet	Cizo Equat			
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	Design a	and development of LASER diode instrument for physiotherapy, using 8051.
	Design	Engineer (2000-2006)
	Black an	d white Ultrasound:
		Ultrasound testing, Maintenance and Production.
		Research and development.
		Project planning, as a hardware team leader.
	Ultrasou	nd Doppler:
		Research and development.
		Project planning, as a hardware team leader.
	OEM EK	G (Electrocardiography) module for ultrasound machine:
		Research and development
		Project planning, as a hardware team leader.
	OEM ulti	asound keyboard
		Research and development
		Testing, Maintenance and production.
		Project planning, as a hardware team leader.
	Artifi	cial kidney firmware using RTX from keil company:
	-	Software development
	Software	e team leader (start at 2006):
	Developi	ng software for ultrasound system
	Develop	Device driver design and implementation
		Scan Converter design and implementation (OpenMP)
		Beamformer algorithm design and implementation (OpenMP)
		Speckle Reduction module based on anisotropic diffusion filter using CUDA
		User interface
	Coffware	Mangar (start at 2008)
	Soltware	ununu ibetech com
		www.ibelech.com
		Souria
		Design and Develop 4D ultrasound system
		Visualization based on OpenGL
		CUDA (filter implementation)
		Signal and image Processing (OpenMP)
		www.ibetech.com
TEACHING EXPERIENCE	<u>C.A.S.B.</u>	<u>L.C.</u> (Cairo university-Faculty of engineering)
		Medical power supply design
	<u>IMC</u>	(Industrial Modernisation Center)
		ARM/ Architecture
	<u>IMC</u>	(Industrial Modernisation Center)
		Embedded Real-Time operating system RTOS
	<u>C.A.S.B.</u>	E.C. (Cairo university-Faculty of engineering)
		VHDL
	<u>SBME</u>	(Cairo university-Faculty of engineering)
		CUDA
	<u>SBME</u>	(Cairo university-Faculty of engineering)
		OpenMP - Multicore parallel programming
	<u>MIU</u>	(Misr International University)
		Ultrasound System Architecture

	CIBEC 2012
	Parallel computing in medical images; multicore and GPU processing
	<u>SBME</u> (Cairo university-Faculty of engineering)
	Device driver design (WDM)
	SBME (Cairo university-Faculty of engineering)
	OpenGL-CG
	<u>CHI</u> (Cairo Higher Institute For Engineering, Computer Sciences And Management) Real-Time Operating System (2014/2015)
	<u>CHI</u> (Cairo Higher Institute For Engineering, Computer Sciences And Management) Introduction to Computer Science II (2014/2015)
	MUST (Misr University for Science & Technology) Advanced Ultrasound Imaging (2014/2015)
	<u>CIBEC 201</u> 4
	Parallel computing in medical images: CPU and GPU processing
Work Experience	International Electronics:
	From 1999 - 2002 Title: bardware Decign engineer
	From 2002 to 2006
	Title: hardware Design team leader.
	From 2006 to 2008
	Title: software team leader
	2007
	M.Sc. Systems and Biomedical Engineering.
	2008
	Title: software manger
	2014
	PHD. Systems and Biomedical Engineering.
	Proficient in English.
Languages	Arabic native language.
PERSONAL DETAILS	
	Military service: attempted.
	Marital Status: married.
	Nationality: Egyptian. Birth Dav: 10/2/1975
	Birdi Day. 10/2/15/5.

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Work skills

PROGRAMMING	
	 Assembly program for ultrasound keyboard, it include PS2 trackball, 8 sliders, 5 Knops and 5 LEDs all using one chip 8051 controller
	- Interface program (Visual C++) for ECG machine
	- USB firmware interfacing ultrasound Machine with PC(<u>Tetrad Corp</u>)
	- USB firmware interfacing EKG module with PC
	- Firmware for ECG QRS detection
	- Software for dialysis machine, ARM7 and RTOS. <u>Haidylena company</u>
	 Firmware for DATALOGGER using cypress PSOC controller, supporting file system
	- PCIE device driver for ultrasound machine using Microsoft DDK tools
	- PC-based beamformer algorithm, OpenMP
	- PC-based scan-converter algorithm, OpenMP
	- Speckle reduction algorithm based on anisotropic diffusion filter, CUDA
	- 4D ultrasound visualization and processing software using OpenGL, CG shading language and CUDA
PCB DESIGN	 PCB design and layout: OEM ECG (<u>TETRAD corporation</u>). Ultrasound DC-DC switched mode power.
PLD PROGRAMING	Small module for testing OEM modules
	FPGA VHDL program for PC-based oscilloscope, xillinx Spartan3. <u>EZ Scope</u>
Hardware Analog design skills	Sliding filter for analog beamformer Logarithmic amplifier for analog beamformer TGC amplifier for analog beamformer
	Dynamic delay element design for analog beamformer
	TR Switch for pulsar circuit for analog beamformer
	Ultrasound board testing and maintenance
	Mixer circuit for spectral Doppler Switched capacitor and switch resistor filters for spectral Doppler
	Hilbert transform using analog filters for audio spectral Doppler.
	DC-DC design
	Electrical safety used for design of ECG module to isolate and protect patient. Sensor interface for dialysis machine
PLANNING AND CONTROL	Good experience in Microsoft project planning.
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SKILLS			
PUBLICATIONS	 Nonparametric clutter rejection in Doppler ultrasound using principal component analysis (SPIE2003) GPU-Based Reconstruction and Display for 4D Ultrasound Data (10, 1109/ULTSYM 2009,0046) 		
	- Multipass GPU Surfac - Optimized GPU Proce Ultrasound Imaging (e Rendering in 4D ultrasound (SIPEC IEEE 2012) ssing Framework for Surface Rendering in 4D J. Med. Imaging Health Inf. 4, 197-202 (2014))	
REFERENCES	Prof: <u>Abo Bakr Youssef</u>	(Professor of Biomedical Engineering Systems & Biomedical Engineering Faculty of Engineering, Cairo University)	
		Phone: +201222174370	
	Prof: <u>Yasser Mustafa</u>	(Professor of Biomedical Engineering Systems & Biomedical Engineering Faculty of Engineering, Cairo University)	
		Phone: +9660541944717	
	Eng: <u>Amr Handy</u> .	Head of R&D in King Faisal Hospital Saudi Arabia	
		Phone: +966593684255/+201111859317	
	Ass Prof: <u>Mai Mabrouk</u>	(Associate Professor and head of Biomedical Engineering Systems & Biomedical Engineering Faculty of Engineering, Masr University of Science and Technology) Phone: +201001662403	
	Prof: <u>Ahmed El-Bialy</u>		
		(Professor and head of Biomedical Engineering Systems & Biomedical Engineering Faculty of Engineering, Cairo Higher Institute) Phone: +201001234080	
	Ass Prof: Mohamed Ebrah	em Owase.	
		(Associate Professor of Biomedical Engineering Systems & Biomedical Engineering Faculty of Engineering, Cairo University) Phone: +201005281106	